

IN THE CLAIMS:

Please cancel claims 1-46 and 68-81 without prejudice or disclaimer.

Please amend claims 66 and 67 as follows:

92 *Ind. B17*
66. (Amended) The nonvolatile semiconductor memory according to claim 65, wherein said fixed potential is a ground potential or a power supply potential.

67. (Amended) The nonvolatile semiconductor memory according to claim 57, wherein said first, second, third and fourth signal lines are bit lines.

REMARKS

Applicants request the Examiner consider the above claims prior to an examination on the merits of the present application.

Respectfully Submitted,

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Dated: February 14, 2002

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